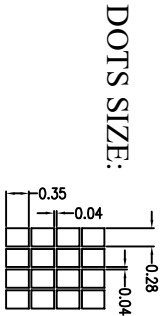
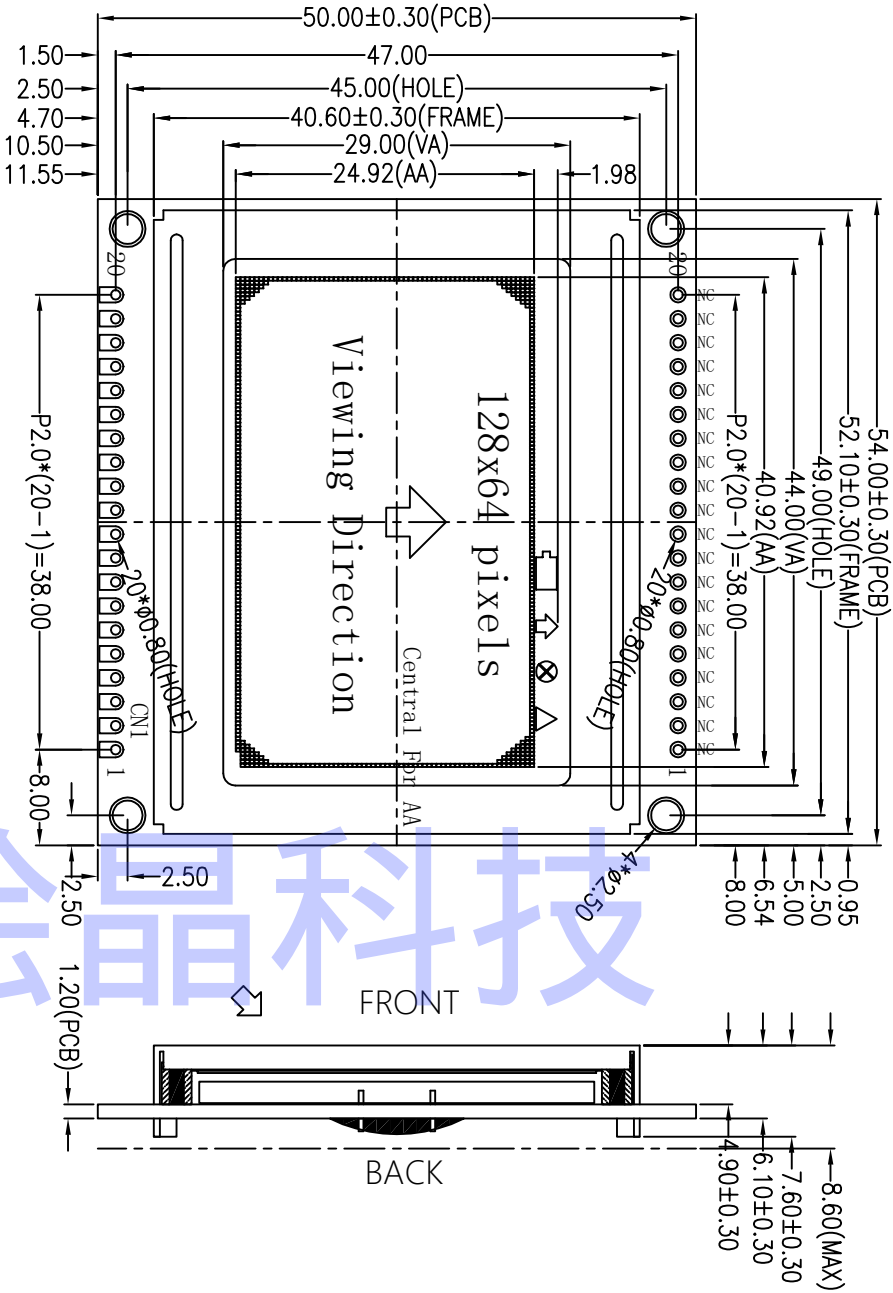
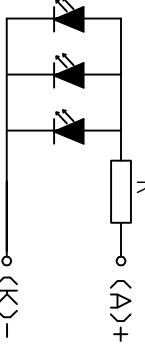


NO./编号	DESCRIPTION/修改说明	DATE/日期
△	First Release/第一版本(补正资料)	2023-07-14

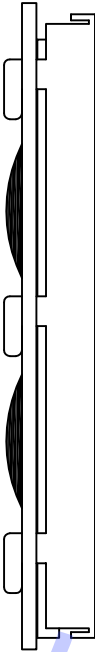
客户确认签名/ APPR. BY Customer	
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BL Circuit Diagram/背光原理



CN1 PIN DEFINITION	
1	VSS Ground for Logic(0V)
2	VDD Power supply for Logic
3	V0 CONTRAST ADJUST
4	RS "H"Data ; "L" Instruction code
5	R/W "H"Read ; "L" Write
6	E Enable signal
7	DB0 Data bus line
8	DB1 Data bus line
9	DB2 Data bus line
10	DB3 Data bus line
11	DB4 Data bus line
12	DB5 Data bus line
13	DB6 Data bus line
14	DB7 Data bus line
15	CS1 Chip 1 Select/Left, High Efficiency
16	CS2 Chip 2 Select/Left, High Efficiency
17	/RST Reset signal
18	VEE NEGATIVE POWER OUT
19	BLA Power supply for LED(+)
20	BLK Power supply for LED(-)

项目/ITEM	参数/PARAMETERS	项目/ITEM	参数/PARAMETERS	 深圳市绘晶科技有限公司 Shenzhen huijing Technology Co. Ltd.	
OPERATING VOLTAGE	5.0V / 3.3V(默认5.0V工作模式)	OPERATING TEMP	-20°C ~ 70°C		
OPERATING CURRENT	15mA(max, WITHOUT BACKLIGHT)	STORAGE TEMP	-30°C ~ 80°C	DRAWN/设计	LM
LCD DRIVE VOLTAGE	9.5 ± 0.30V(VDD-V0) TBD	BACKLIGHT	VLED=5.0V, White, If≤45mA	CHECKED/检查	ZPF
DISPLAY MODE	STN-BLUE, NEGATIVE, TRANSMISSIVE	ROHS STANDARD	YES ☒ NO ☐	APPROVED/批准	YXS
LCD DRIVE METHOD	DUTY: 1/64, BIAS: 1/9	CONNECTION	PCB hole	TEL/电话	0755-23146001
Driver/Controller	AIP3T108 OR EQUIV	UNSPECIFIED	± 0.2mm	FAX/传真	0755-23146002
VIEW DIRECTION	6:00 O'CLOCK	Third perspective		Address/地址	深圳市宝安区石岩街道田寮社区工业大道中(官田科技园)4栋4层, Building 4D, Huijing Guanlian Science and Technology Park, Guanlian Hongpeng Industrial Avenue, Shiyan Street, Bao'an District, Shenzhen